



**Courses on
Photonic Integrated Circuits Design, Fabrication & Packaging
2nd Edition, a.y. 2016/2017**

**Photonic Integrated Circuit Design and Fabrication (PIC D&F)
Photonic and Electronic Integrated Circuit Packaging (PEIC Pack)
Characterization and Testing (C&T) session**

LIST OF ADMITTED APPLICANTS

Surname	Name	Selected Courses
Alemaný Server	Ruben	PIC D&F; PEIC Pack; C&T session
Ali	Muhsin	PIC D&F; PEIC Pack; C&T session
Artiglia	Massimo	PIC D&F; C&T session
Bastos Pereira	Ricardo Nuno	PEIC Pack
Bru Orgiles	Luis Alberto	PIC D&F; PEIC Pack; C&T session
Falconi	Fabio	PIC D&F; PEIC Pack; C&T session
Gonzalez Gonzalez	Raul	PIC D&F; PEIC Pack; C&T session
Imram	Muhammad	PIC D&F; PEIC Pack; C&T session
Kaplan	Ali Emre	PIC D&F; PEIC Pack; C&T session
Kota	Madhava Reddy	PIC D&F; PEIC Pack; C&T session
Lo	Mu-Chieh	PIC D&F; PEIC Pack; C&T session
Maia Tavares	Ana Cristina	PEIC Pack
Malacarne	Antonio	PIC D&F, C&T session
Mico Cabanes	Gloria	PIC D&F; PEIC Pack; C&T session

Pisa, April 28th 2017